

IN THE CLAIMS

1. (Currently Amended) An apparatus comprising:
a universal serial bus (USB) host controller capable of coupling a plurality of queue heads to a frame list,
wherein the plurality of queue heads are directly coupled to the frame list during initialization before coupling any split-isochronous transaction descriptors to the plurality of queue heads where split-isochronous transaction descriptors are supported.

2. (Original) The apparatus of claim 1, further including a host controller driver.
3. (Original) The apparatus of claim 1, wherein the plurality of queue heads are coupled to the frame list before any transaction descriptors during initialization of the host controller.
4. (Original) The apparatus of claim 1, wherein the plurality of queue heads are coupled to the frame list before any transaction descriptors after initialization of the host controller.

- 5-6 (Canceled)

7. (Original) The apparatus of claim 6, where the host controller is a USB 2.0 host controller.

8. (Currently Amended) A system comprising:
a first universal serial bus (USB) host controller and a second USB host controller, said first host controller capable of coupling a plurality of queue heads to a frame list, and
a device coupled to said first and second host controllers,

wherein the plurality of queue heads are directly coupled to the frame list during initialization before coupling any split-isochronous transaction descriptors to the plurality of queue heads where split-isochronous transaction descriptors are supported.

9. (Original) The system of claim 8, further including:

a first host controller driver associated with said first host controller, and
a second host controller driver associated with said second host controller.

10. (Original) The system of claim 8, wherein the plurality of queue heads are coupled to the frame list before any transaction descriptors during initialization of the first host controller.

11. (Original) The system of claim 8, wherein the plurality of queue heads are coupled to the frame list before any transaction descriptors after initialization of the first host controller.

12-13 (Canceled)

14. (Currently Amended) The system of claim 138, where the first host controller is a USB 2.0 host controller.